

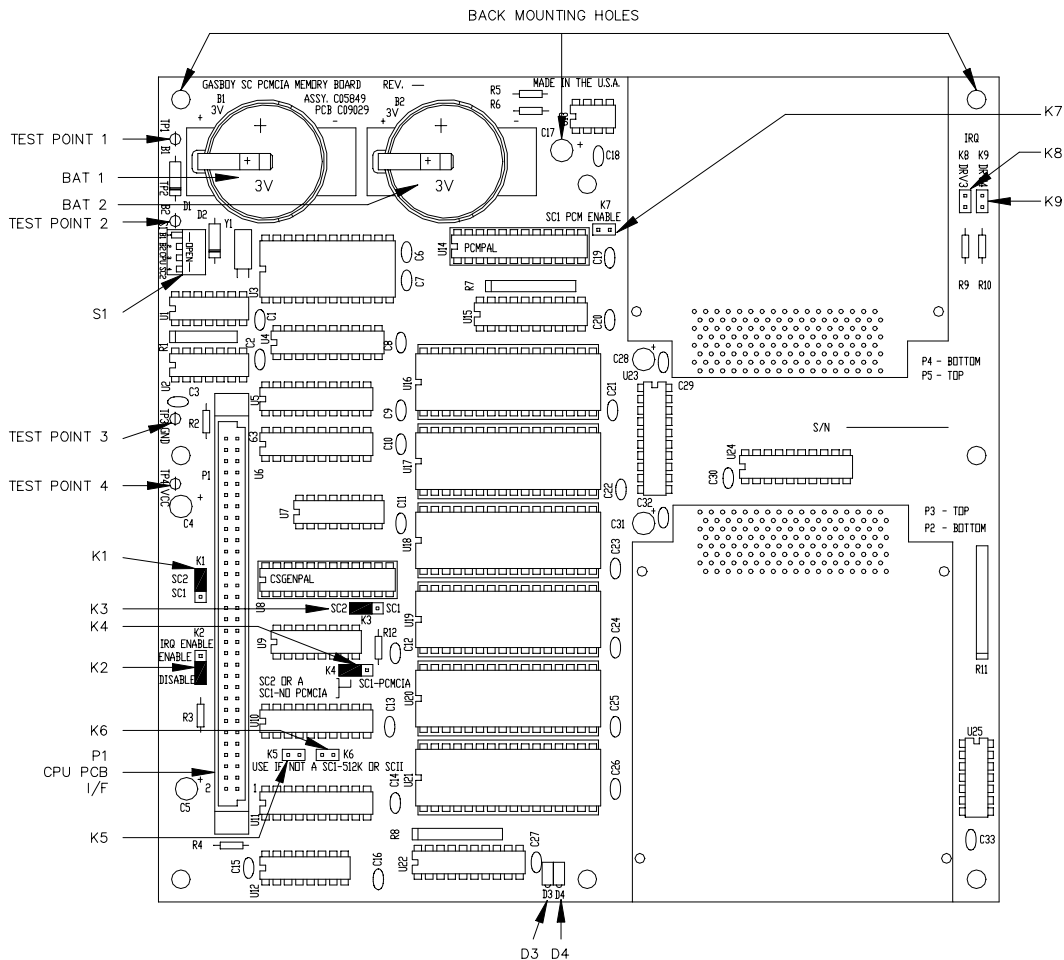
SITE CONTROLLER II CPU INSTRUCTION SHEET

Use the following kits (C07047 and C07048) to replace the CPU board in a site controller. Each kit contains the following:

- Site Controller II CPU Instruction Sheet, C35381
- CPU board assembly C05852 (kit C07047) or C05328 (kit C07048)
- PAL replacement kit containing CSGEN2a or CSGEN3

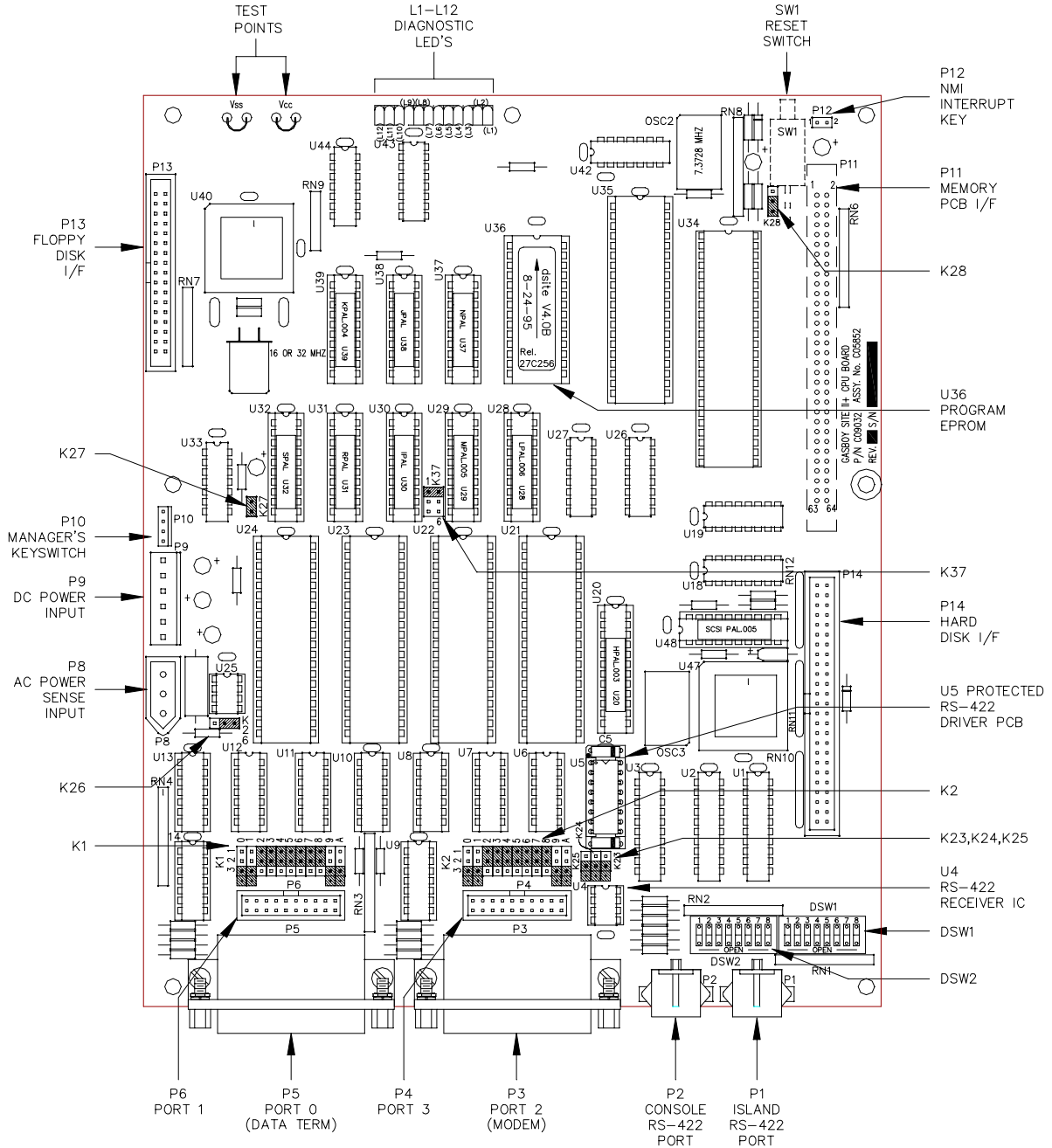
1. Before installing the new CPU board, make sure **ALL** data has been polled and/or backed up. If the system is a site controller II on a bank network, make sure a settlement has been done and completed without failure. Turn off power and unplug the site controller.
2. Remove the four Phillips screws from the sides of the site controller and remove the cover.
3. Disconnect all cables going to the CPU board (top board), making note of which cable goes to which connector. Remove the Phillips screws that secure the site controller CPU board and carefully remove the board.
4. Refer to the chart at right and insert the proper CSGEN chip on the memory board. Refer to drawing below for placement of CSGEN PAL. If memory board does not match drawing, do not change PAL.

Locations	C05328 CPU	C05852 CPU
CPU Board U28	No change	LPAL6
Memory Board U8	CSGEN2a	CSGEN3



5. Set the jumpers on the CPU board to match the site's configuration.
6. Reassemble the site controller. Plug in the site controller and turn on power. Verify the site is working correctly.

SITE CONTROLLER II CPU PCB - C05852 OR CR5852

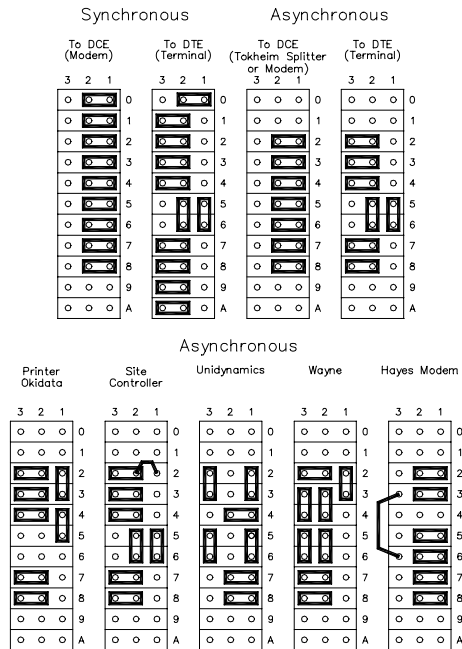


NOTE: C05852/CR5852 CPU PCB requires operating system version 2.0C or above, new memory PCB, C07041, and Dsite 4.0B or above. If the CPU PCB is being installed at a Profit Point site, the Profit Point must be updated to version 2.0 or above.

K23-K37 CPU BOARD JUMPERS - C05852 ONLY

Jumper	Function	Status
K23	Synchronous comm signals disabled at modem port	Open
K24	Synchronous comm signals disabled at modem port	Open
K25	Synchronous comm signals disabled at modem port	Open
K26	AC power fail signal enabled	1-2 Open
		2-3 Jumpered
K27	Deadman timer enabled	Jumpered
K28	Soft reset enabled	1-2 Jumpered
		2-3 Open
K37	Number of wait states for PCMCIA RAM accesses	1-2 Jumpered
		3-4 Open
		5-6 Open

K1 & K2, PORT 1 & PORT 3 CONFIGURATION JUMPERS - C05852/CR5852 OR C05328/CR5328



CPU PCB SWITCHES C05852/CR5852 OR C05328/CR5328

DSW1 - Backup Sign-on, Hard Disk Access, Boot Modes

Switch	Function	Setting
DSW1-1	Backup sign-on disabled	Open
DSW1-2	Hard disk	Open-Disabled
		Closed-Enabled
DSW3-3	Not used	Don't care
DSW1-4	Not used	Don't care
DSW1-5	Diagnostic program disabled	Open
DSW1-6	Not used	Don't care
DSW1-7	See below	
DSW1-8	See below	

DSW1-7 & DWS1-8 Mode Set Switches

Switches		Modes		
DSW1-7	DSW1-8	Reset	LED's	Crash
OPEN	OPEN	BOOT	NORMAL	BOOT
OPEN	CLOSED	BOOT	NORMAL	MONITOR
CLOSED	OPEN	BOOT	SCAN	BOOT
CLOSED	CLOSED	MONITOR	SCAN	MONITOR

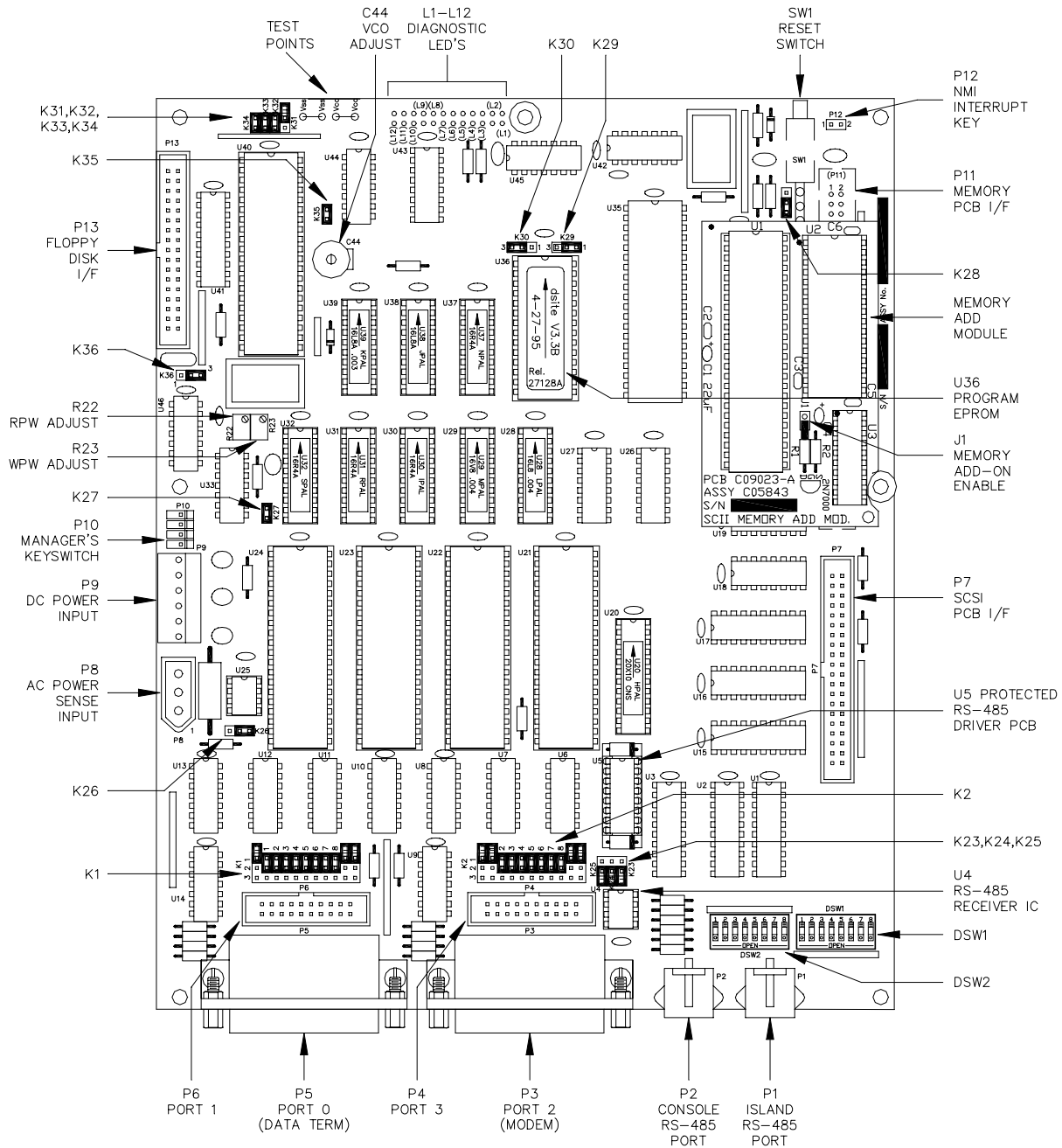
DSW2 - Baud Rates

PORT	SWITCH	BAUD RATES			
		300	1200	2400	9600
LOCAL	DSW2-1	OPEN	CLOSED	OPEN	CLOSED
	DSW2-2	OPEN	OPEN	CLOSED	CLOSED
REMOTE	DSW2-3	OPEN	CLOSED	OPEN	CLOSED
	DSW2-4	OPEN	OPEN	CLOSED	CLOSED
SUBSITES	DSW2-5	OPEN	CLOSED	OPEN	CLOSED
	DSW2-6	OPEN	OPEN	CLOSED	CLOSED
LOG PRINTER	DSW2-7	OPEN	CLOSED	OPEN	CLOSED
	DSW2-8	OPEN	OPEN	CLOSED	CLOSED

K23-K36 CPU BOARD JUMPERS - C05328 ONLY

Jumper	Function	Status
K23	Synchronous comm signals disabled at modem port	Open
K24	Synchronous comm signals disabled at modem port	Open
K25	Synchronous comm signals disabled at modem port	Open
K26	AC power fail signal enabled	1-2 Open
		2-3 Jumpered
K27	Deadman timer enabled	Jumpered
K28	Soft reset enabled	1-2 Jumpered
		2-3 Open
K29	U36 is a 27128 EPROM for DSITE V2.4 and below U36 is a 27256 EPROM for DSITE V3.1A and above	1-2 Open for 27128 EPROM
		1-2 Jumpered for 27256 EPROM
		2-3 Jumpered for 27128 EPROM
		2-3 Open for 27256 EPROM
K30	U36 is a 27128 EPROM for DSITE V2.4 and below U36 is a 27256 EPROM for DSITE V3.1A and above	1-2 Open
		2-3 Jumpered
K31	Floppy drive normal operating mode enabled	Open
K32	Floppy drive MFM recording enabled	Jumpered
K33	Floppy drive pre-compression enabled	Jumpered
K34	Floppy drive is 3-1/2" or 5-1/4"	Jumpered
K35	POR signal to U28	Jumpered
K36	FDC READY signal from disk drive	1-2 Open
		2-3 Jumpered

SITE CONTROLLER II CPU PCB - C05328 OR CR5328



JUMPER - C05328/CR5328 ONLY

Jumper	Function	Status
J1	Memory Add-on Enable	Jumpered—Memory disabled (OS V1.0 and below) Open—Memory enabled (OS V2.0 and above)